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FIG. 1 PRIOR ART

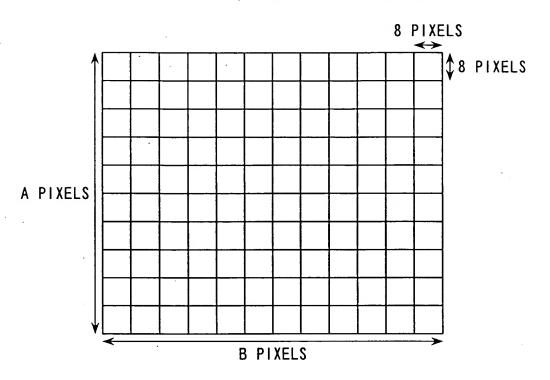
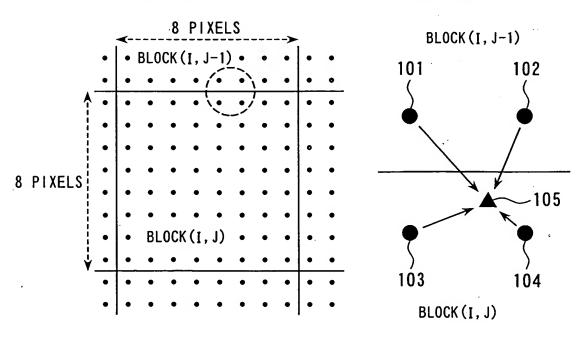


FIG. 2A PRIOR ART FIG. 2B PRIOR ART



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FIG. 3 PRIOR ART

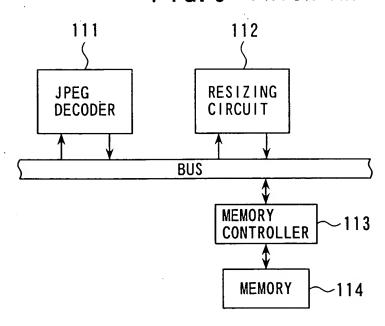
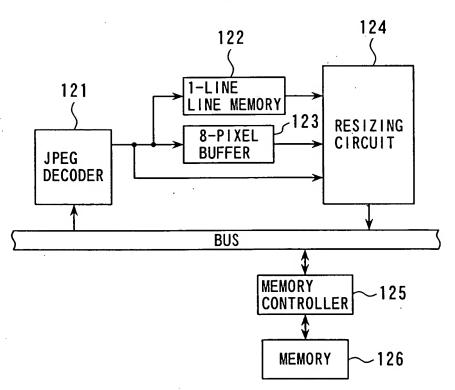


FIG. 4 PRIOR ART



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FIG. 5 PRIOR ART

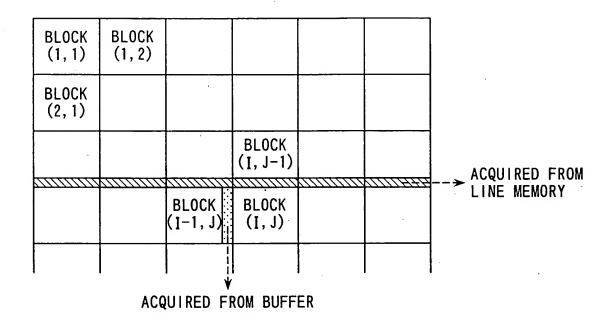


FIG. 6

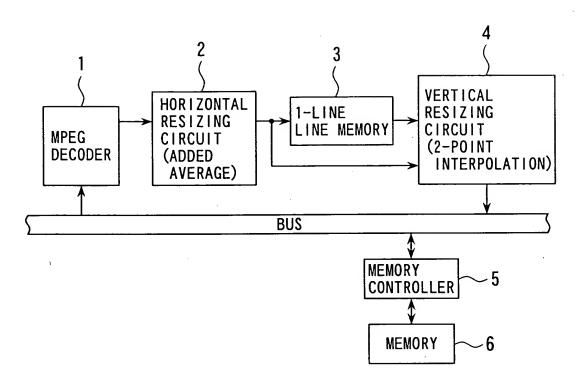


FIG. 7

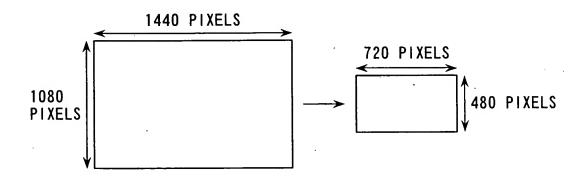
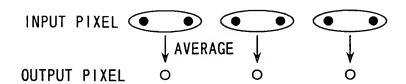
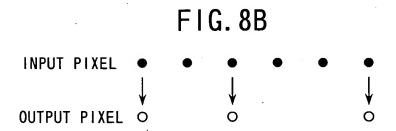


FIG. 8A





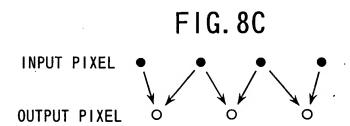


FIG. 8D

INPUT PIXEL

OUTPUT PIXEL

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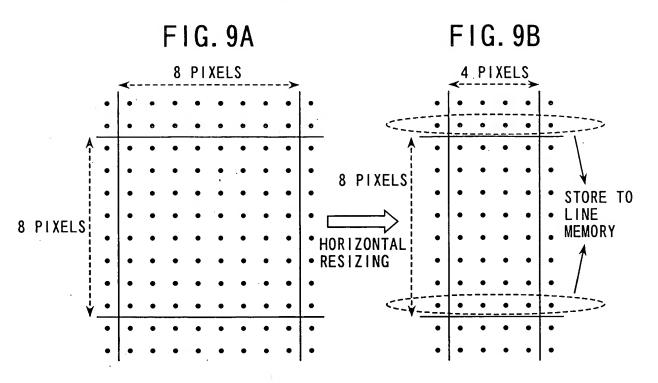
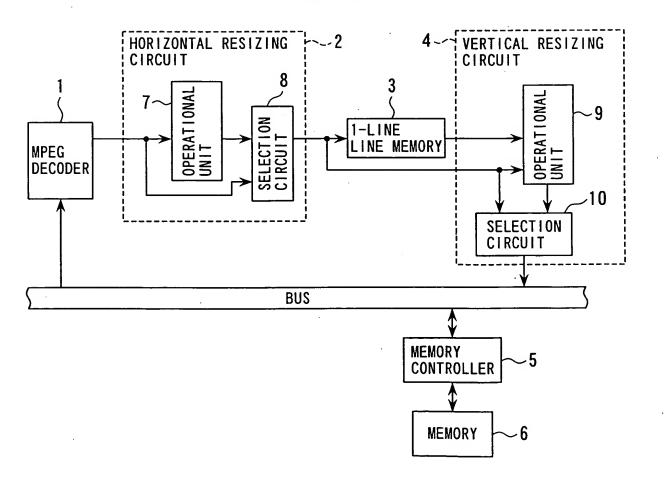


FIG. 10



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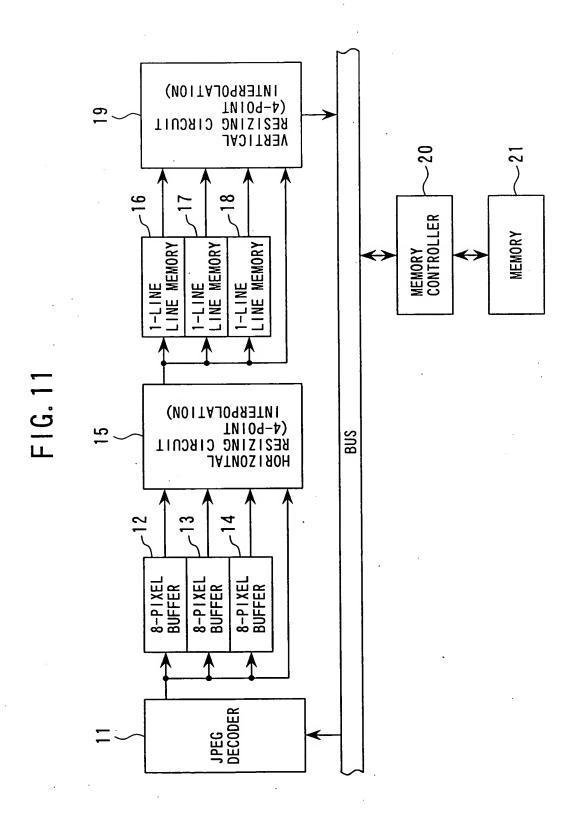
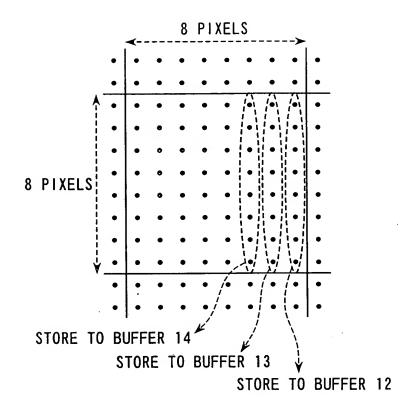
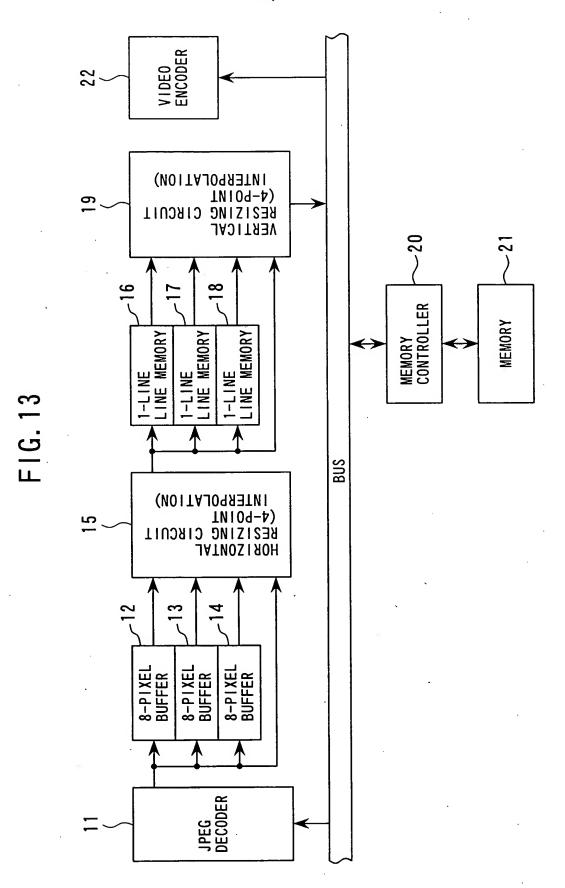


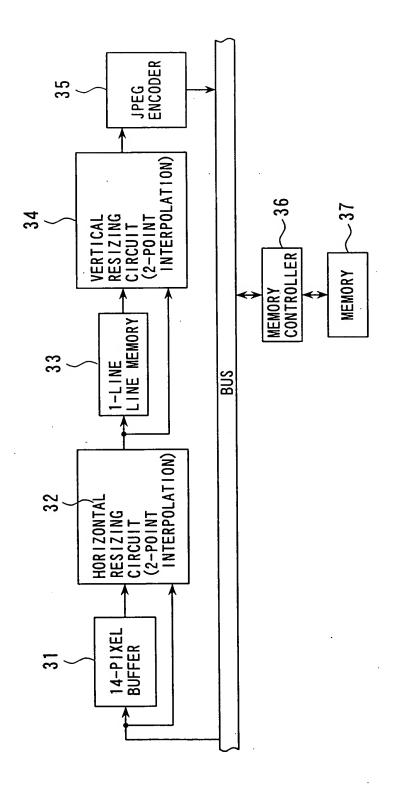
FIG. 12







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F1G. 14

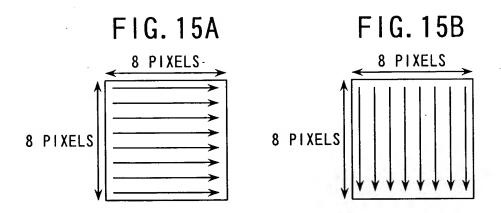


FIG. 15C

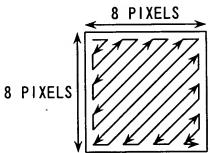


FIG. 15D

